

CONVERTING SIGNALS FROM A LOW VOLTAGE DOMAIN
TO A HIGH VOLTAGE DOMAIN

ABSTRACT OF THE DISCLOSURE

A method for converting a signal from a low voltage domain to a high voltage domain includes receiving an input signal in a low voltage domain, and using the input signal, controlling a first transistor having a first carrier type, a second transistor having a second carrier type different from the first carrier type, and a third transistor having the second carrier type to produce an output voltage at an output terminal. The first transistor is coupled to the output terminal and further coupled to a first voltage corresponding to a first value in a high voltage domain. The second and third transistors are coupled in series between the output terminal and a second voltage corresponding to a second value in the high voltage domain. The output voltage is selected to correspond to either the first voltage or the second voltage based upon the input signal.